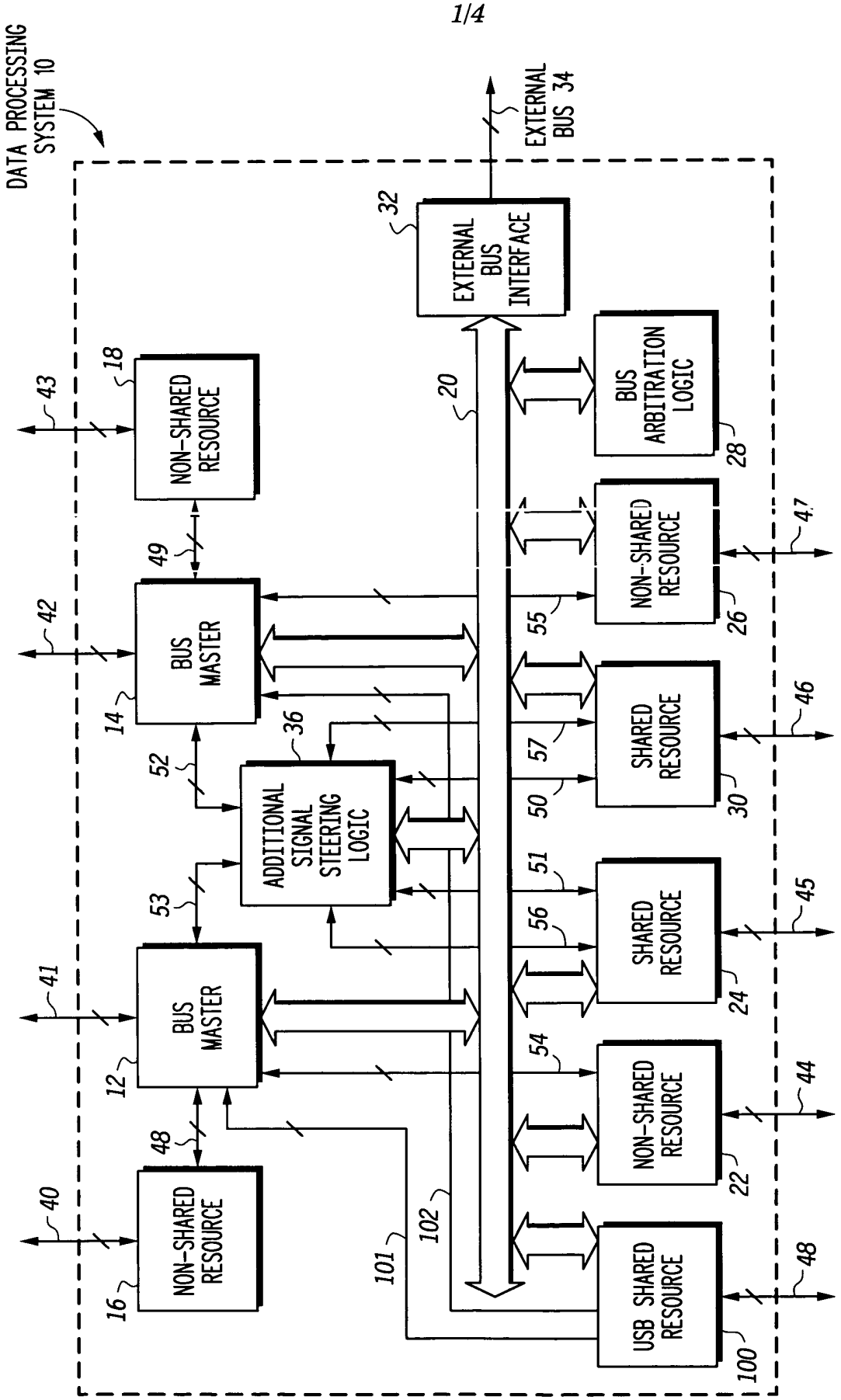
DATA PROCESSING SYSTEM 10
EXTERNAL BUS 34
EXTERNAL BUS INTERFACE
32
NON-SHARED RESOURCE
18
BUS ARBITRATION LOGIC
28
BUS MASTER
14
BUS MASTER
12
ADDITIONAL SIGNAL STEERING LOGIC
36
NON-SHARED RESOURCE
26
SHARED RESOURCE
30
SHARED RESOURCE
24
NON-SHARED RESOURCE
22
NON-SHARED RESOURCE
16
USB SHARED RESOURCE
100
20
101
102
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57

FIG. 1

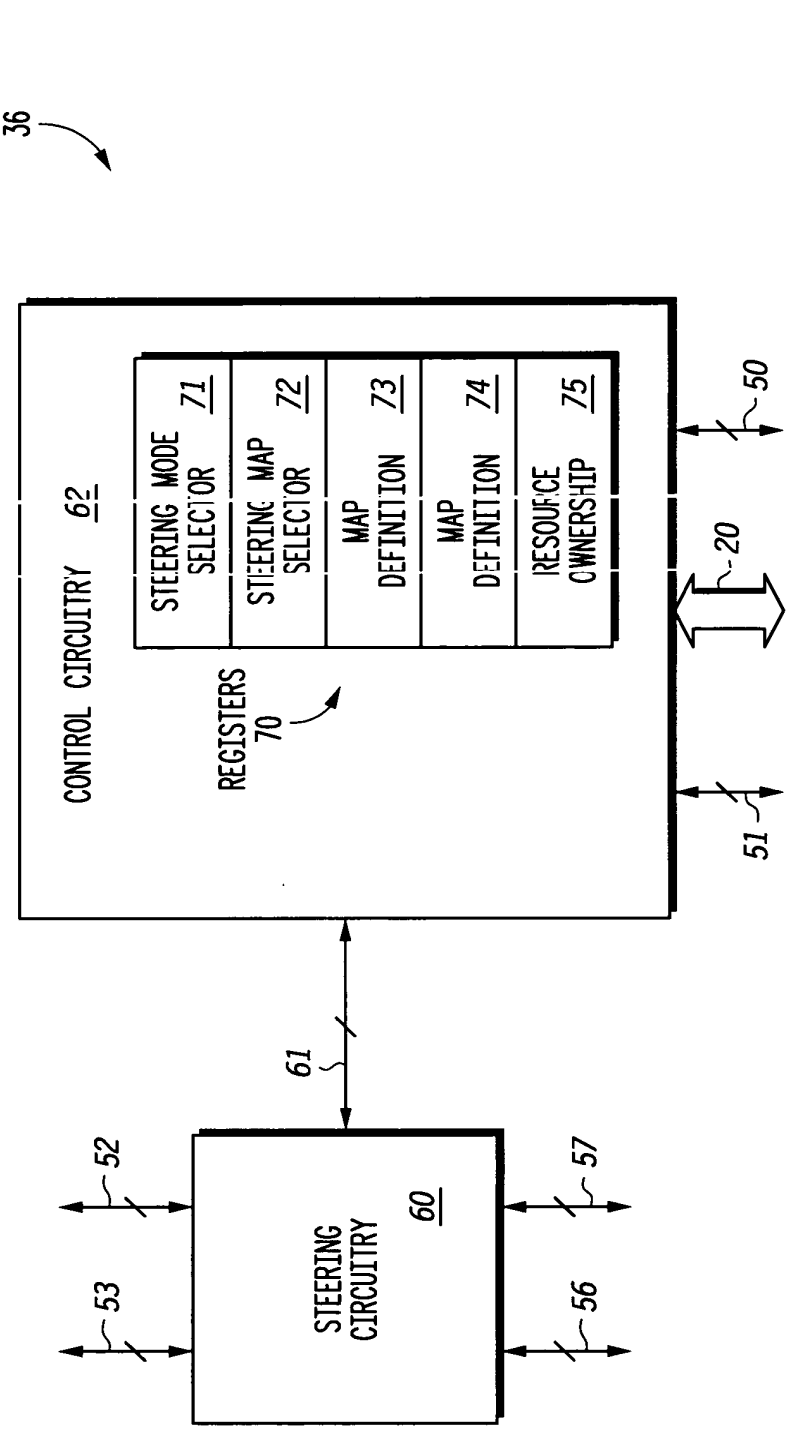
PORTION OF ADDITIONAL SIGNAL STEERING LOGIC 36
CONTROL CIRCUITRY 62
STEERING MODE SELECTOR 71
STEERING MAP SELECTOR 72
MAP DEFINITION 73
MAP DEFINITION 74
RESOURCE OWNERSHIP 75
REGISTERS 70
50
20
51
61
52
53
STEERING CIRCUITRY 60
57
56

FIG. 2

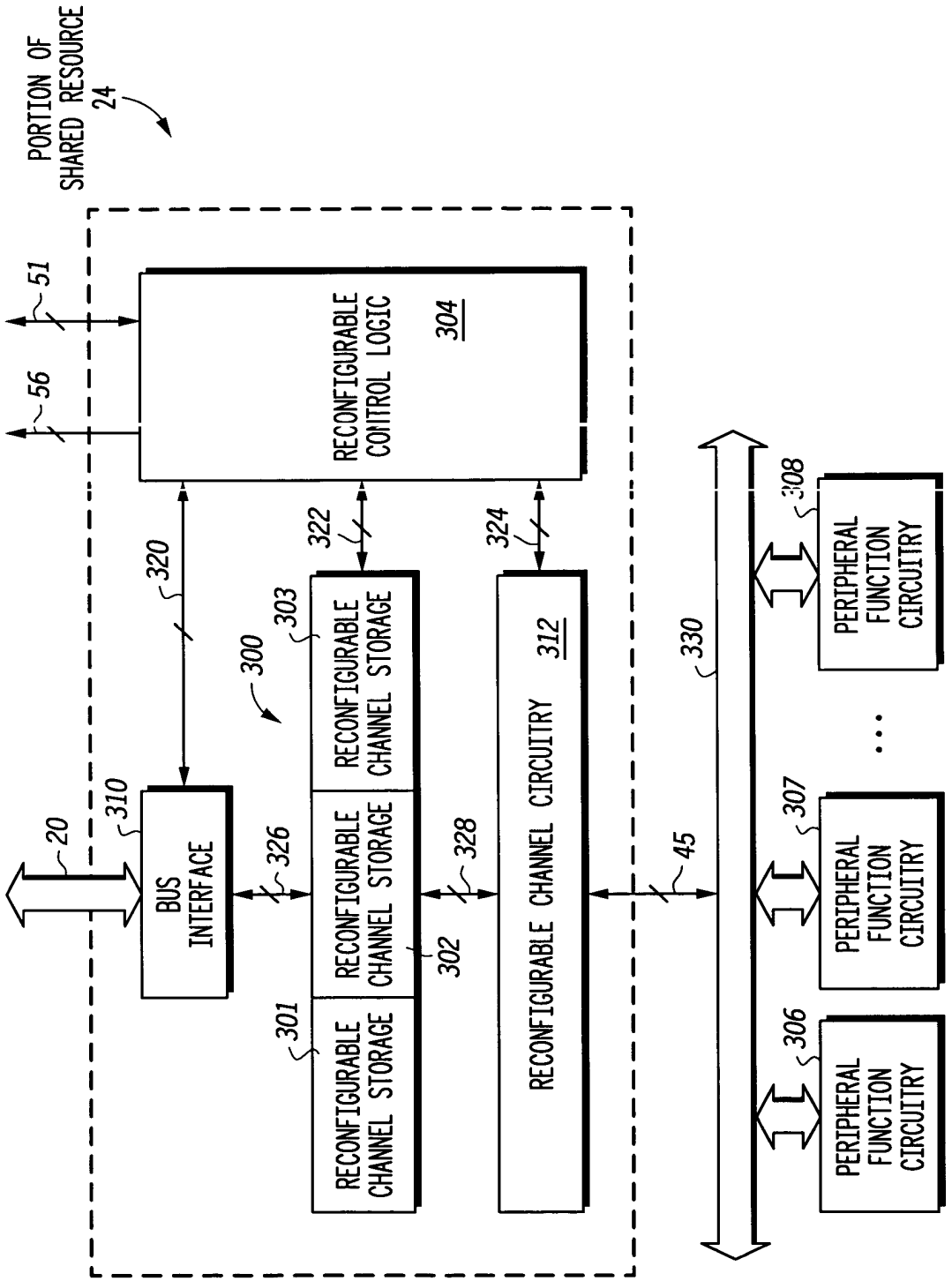
PORTION OF SHARED RESOURCE 24
51
56
RECONFIGURABLE CONTROL LOGIC 304
320
322
324
303
300
RECONFIGURABLE CHANNEL STORAGE
RECONFIGURABLE CHANNEL CIRCUITRY 312
330
308
PERIPHERAL FUNCTION CIRCUITRY
20
310
BUS INTERFACE
326
328
45
307
PERIPHERAL FUNCTION CIRCUITRY
RECONFIGURABLE CHANNEL STORAGE
302
301
306
PERIPHERAL FUNCTION CIRCUITRY

FIG. 3

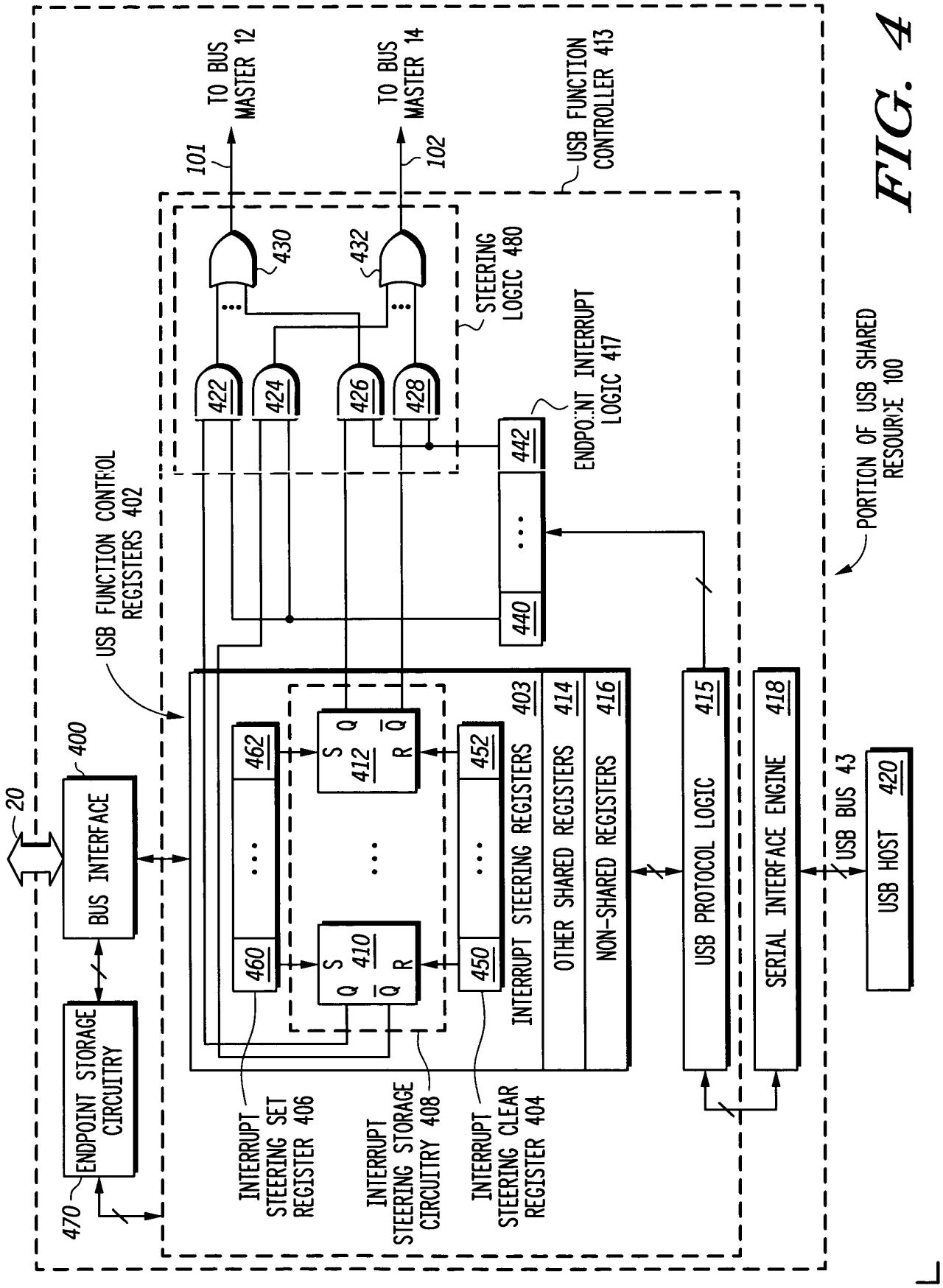
PORTION OF USB SHARED RESOURCE 100
USB FUNCTION CONTROLLER 413
TO BUS MASTER 12
TO BUS MASTER 14
101
102
STEERING LOGIC 480
ENDPOINT INTERRUPT LOGIC 417
USB FUNCTION CONTROL REGISTERS 402
BUS INTERFACE
400
20
ENDPOINT STORAGE CIRCUITRY
470
INTERRUPT STEERING SET REGISTER 406
INTERRUPT STEERING STORAGE CIRCUITRY 408
INTERRUPT STEERING CLEAR REGISTER 404
INTERRUPT STEERING REGISTERS 403
OTHER SHARED REGISTERS 414
NON-SHARED REGISTERS 416
USB PROTOCOL LOGIC 415
SERIAL INTERFACE ENGINE 418
USB BUS 43
USB HOST 420
422
424
426
428
430
432
440
442
450
452
460
462
410
412

FIG. 4